

1N4148W

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1N4148W

400mW Surface Mount Switching Diode 100V

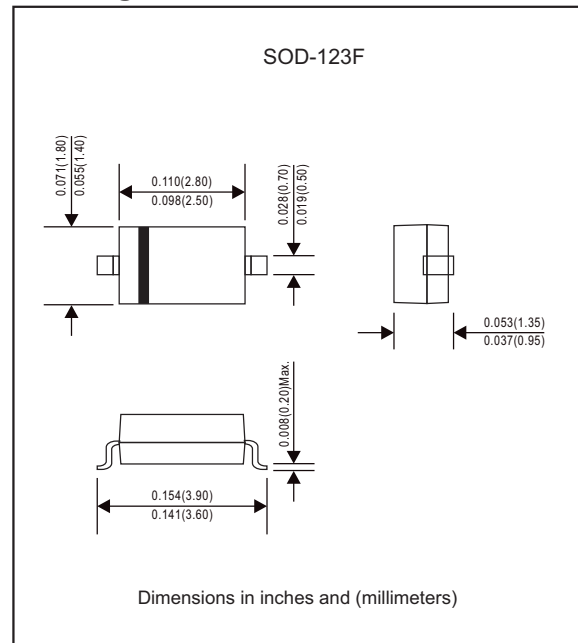
Features

- Fast switching speed
- Electrically identical to standard JEDEC
- Surface mount package ideally suited for automatic insertion
- Tiny plastic SMD package
- High conductance
- Silicon epitaxial planar chip
- Lead-free parts meet RoHS requirements
- Suffix "-H" indicates Halogen free part, ex. 1N4148W-H

Mechanical data

- Epoxy:UL94-VO rated flame retardant
- Case : Molded plastic, SOD-123F
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Polarity : Indicated by cathode band
- Mounting Position : Any
- Weight : Approximated 0.010 gram

Package Outline



Maximum ratings (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	1N4148W	Unit
Reverse voltage	V_R	75	V
Peak reverse voltage	V_{RM}	100	V
RMS voltage	V_{RMS}	50	V
DC blocking voltage	V_{DC}	75	V
Average forward current at $T_A = 25^{\circ}\text{C}$	I_O	150	mA
Peak forward surge current, 1.0 μs	I_{FSM}	2	A
Power dissipation derate above 25°C	P_D	400	mW
Typical junction capacitance (Note 1)	C_J	1.5	pF
Thermal resistance junction to ambient	$R_{\theta JA}$	312	$^{\circ}\text{C/W}$
Operating junction temperature range	T_J	-55 to +150	$^{\circ}\text{C}$
Storage temperature range	T_{STG}	-55 to +150	$^{\circ}\text{C}$

Electrical characteristics (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	1N4148W	Unit
Maximum forward voltage at $I_F = 0.001\text{A}$ at $I_F = 0.01\text{A}$ at $I_F = 0.05\text{A}$ at $I_F = 0.15\text{A}$	V_F	0.715 0.855 1.0 1.25	V
Maximum DC reverse current at rated DC blocking voltage, $T_J = 25^{\circ}\text{C}$ at $V_R = 20\text{V}$ at $V_R = 75\text{V}$	I_R	0.025 2.5	μA
Maximum reverse recovery time (Note 2)	t_{rr}	4.0	ns

Notes 1: C_J at $V_R=0\text{V}$, $f=1\text{MHz}$

2: From $I_F=10\text{mA}$ to $I_R=1\text{mA}$, $V_R=6\text{V}$, $R_L=100\Omega$

Rating and characteristic curves (1N4148W)

Fig. 1 TYPICAL FORWARD CHARACTERISTICS

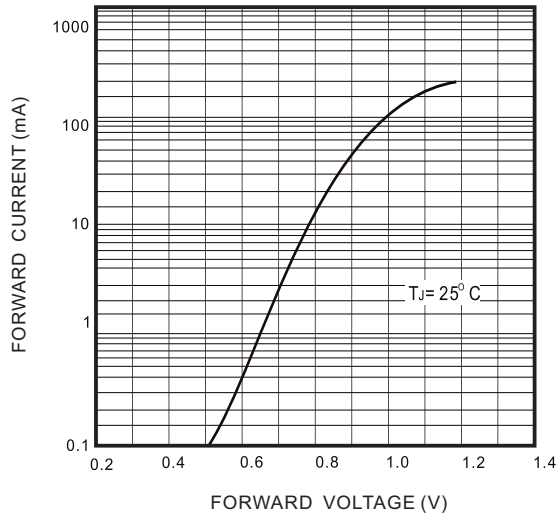


Fig. 2 TYPICAL REVERSE CHARACTERISTICS

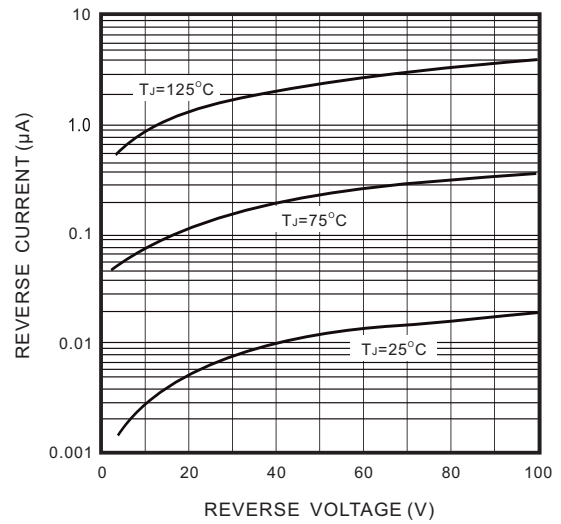


Fig.3 TYPICAL JUNCTION CAPACITANCE

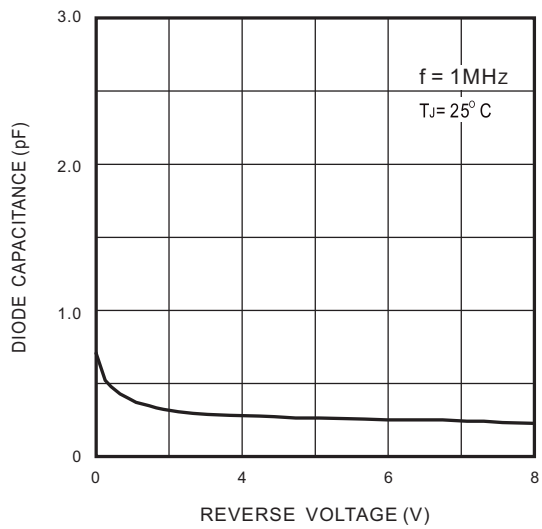
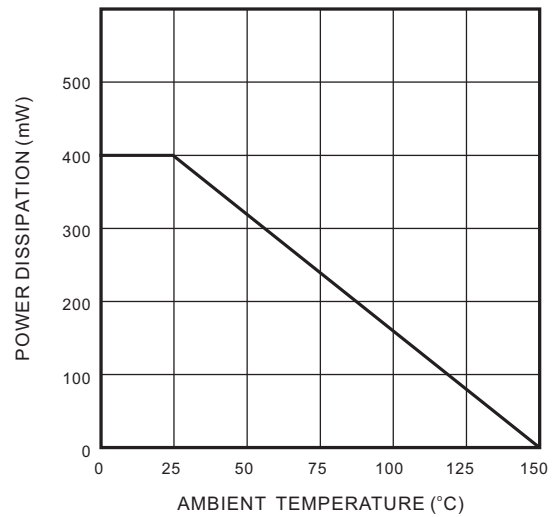


Fig. 4 POWER DERATING CURVE



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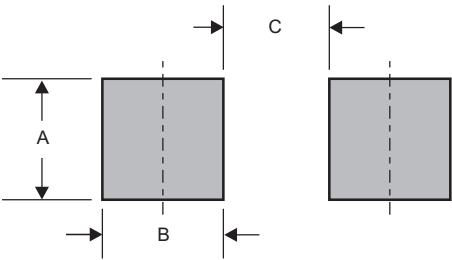
Pinning information

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode		

Marking

Type number	Marking code
1N4148W	A2, T4

Suggested solder pad layout

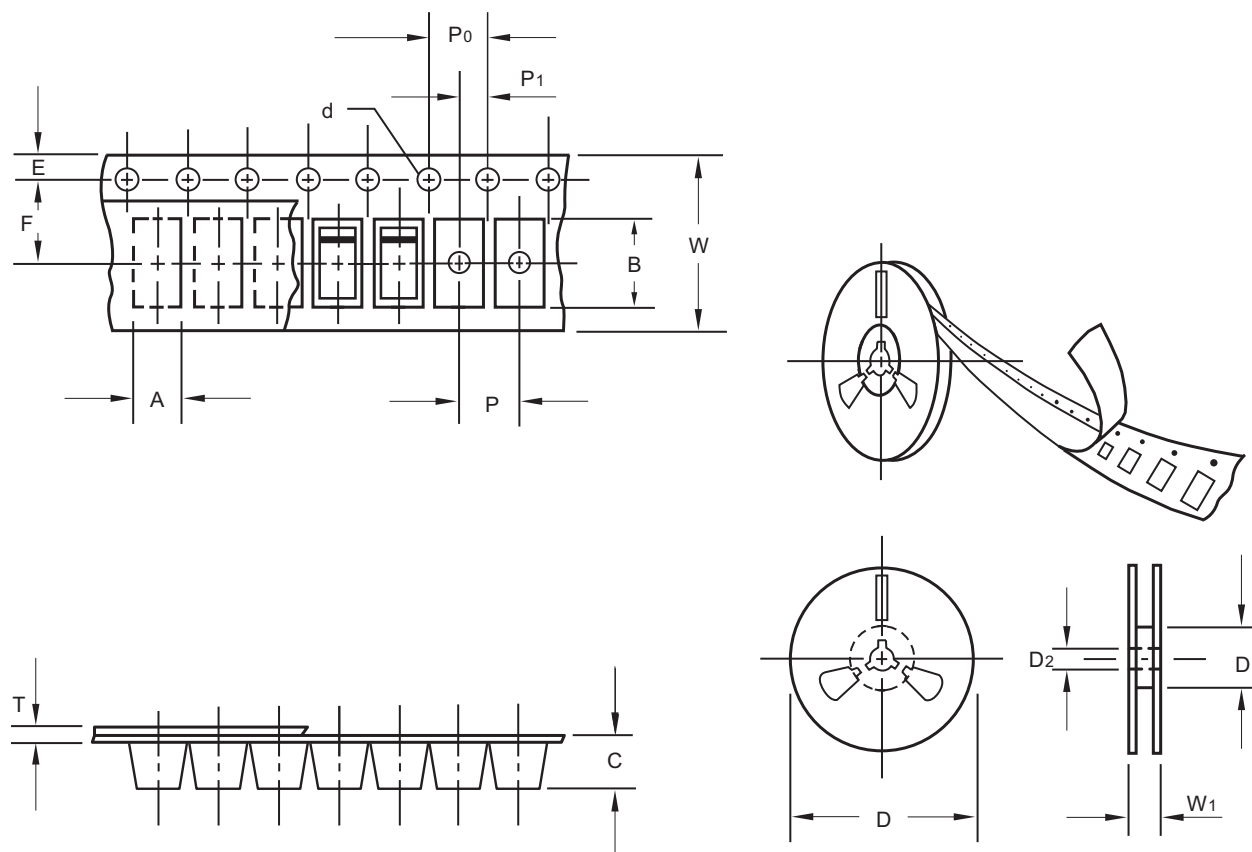


Dimensions in inches and (millimeters)

PACKAGE	A	B	C
SOD-123F	0.048 (1.22)	0.036 (0.91)	0.093 (2.36)

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Packing information



unit:mm

Item	Symbol	Tolerance	SOD-123F
Carrier width	A	0.1	2.00
Carrier length	B	0.1	3.85
Carrier depth	C	0.1	1.10
Sprocket hole	d	0.1	1.50
13" Reel outside diameter	D	2.0	-
13" Reel inner diameter	D1	min	-
7" Reel outside diameter	D	2.0	178.00
7" Reel inner diameter	D1	min	62.00
Feed hole diameter	D2	0.5	13.00
Sprocket hole position	E	0.1	1.75
Punch hole position	F	0.1	3.50
Punch hole pitch	P	0.1	4.00
Sprocket hole pitch	P0	0.1	4.00
Embossment center	P1	0.1	2.00
Overall tape thickness	T	0.1	0.23
Tape width	W	0.3	8.00
Reel width	W1	1.0	11.40

Note: Devices are packed in accordance with EIA standard RS-481-A and specifications listed above.

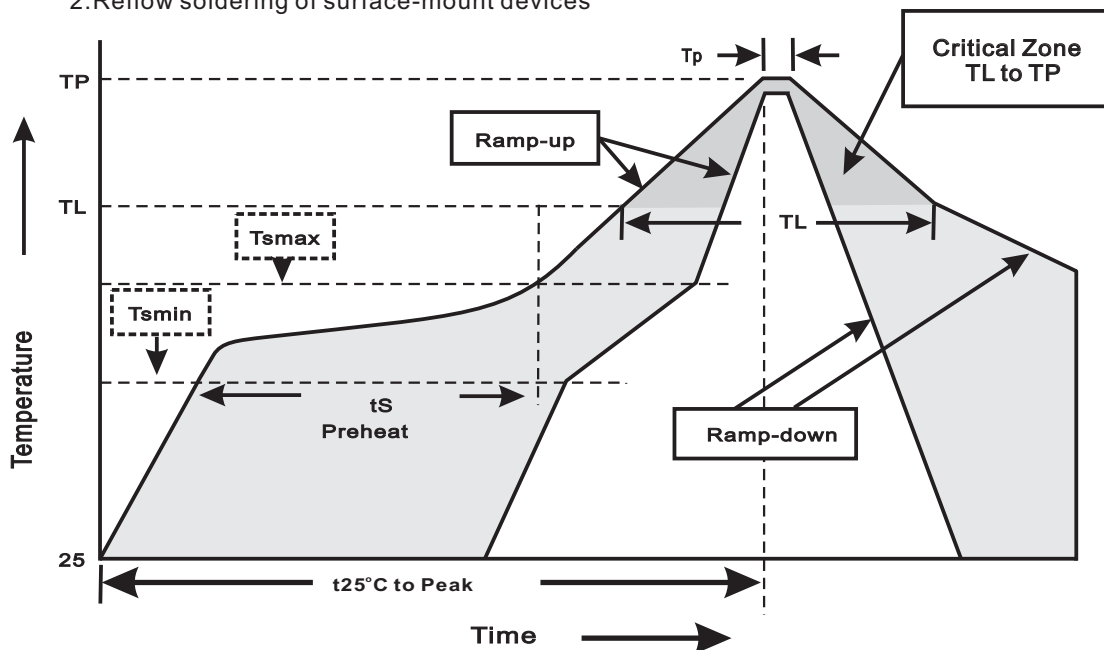
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Reel packing

PACKAGE	REEL SIZE	REEL (pcs)	COMPONENT SPACING (m/m)	BOX (pcs)	INNER BOX (m/m)	REEL DIA, (m/m)	CARTON SIZE (m/m)	CARTON (pcs)	APPROX. GROSS WEIGHT (kg)
SOD-123F	7"	3,000	4.0	30,000	183*123*183	178	382*257*387	240,000	9.5

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(T _L to T _P)	<3°C/sec
Preheat -Temperature Min(T _{smmin}) -Temperature Max(T _{smmax}) -Time(min to max)(t _s)	150°C 200°C 60~120sec
T _{smmax} to T _L -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(T _L) -Time(t _L)	217°C 60~260sec
Peak Temperature(T _P)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(t _P)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes

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High reliability test capabilities

Item Test	Conditions	Reference
1. Solder Resistance	at 260±5°C for 10±2sec.	MIL-STD-750D METHOD-2031
2. Solderability	at 245±5°C for 5 sec.	MIL-STD-202F METHOD-208
3. High Temperature Reverse Bias	$V_R=80\%$ rate at $T_J=150^\circ\text{C}$ for 168 hrs.	MIL-STD-750D METHOD-1038
4. Forward Operation Life	Rated average rectifier current at $T_A=25^\circ\text{C}$ for 500hrs.	MIL-STD-750D METHOD-1027
5. Intermittent Operation Life	$T_A = 25^\circ\text{C}$, $I_F = I_O$ On state: power on for 5 min. off state: power off for 5 min. on and off for 500 cycles.	MIL-STD-750D METHOD-1036
6. Pressure Cooker	$15P_{SIG}$ at $T_A=121^\circ\text{C}$ for 4 hrs.	JESD22-A102
7. Temperature Cycling	-55°C to +125°C dwelled for 30 min. and transferred for 5min. total 10 cycles.	MIL-STD-750D METHOD-1051
8. Forward Surge	Peak forward surge current	MIL-STD-750D METHOD-4066-2
9. Humidity	at $T_A=85^\circ\text{C}$, RH=85% for 1000hrs.	MIL-STD-750D METHOD-1021
10. High Temperature Storage Life	at 175°C for 1000 hrs.	MIL-STD-750D METHOD-1031