

LL4148 (MM4148)

List

List.....	1
Package outline.....	2
Features.....	2
Mechanical data.....	2
Maximum ratings and Electrical characteristics	2
Rating and characteristic curves.....	3
Pinning information.....	4
Marking.....	4
Suggested solder pad layout.....	4
Packing information.....	5
Reel packing.....	6
Suggested thermal profiles for soldering processes.....	6
High reliability test capabilities.....	7

LL4148 (MM4148)

500mW Surface Mount Switching Diode 100V

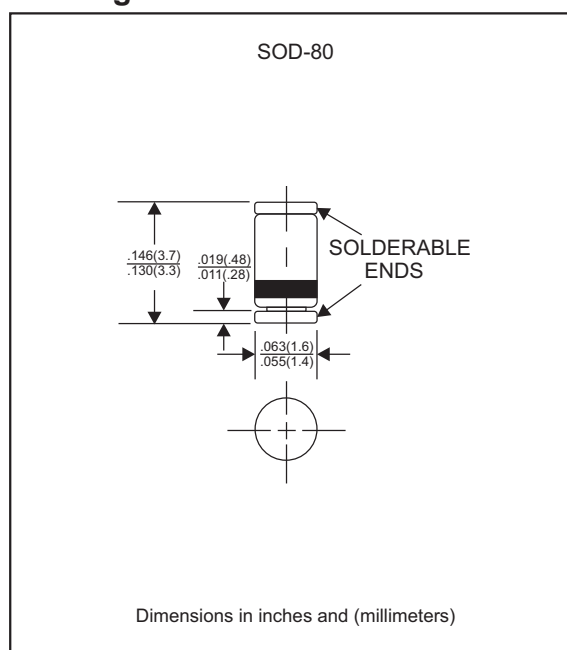
Features

- Fast speed switching
- Silicon epitaxial planar chip construction
- Hermetically sealed glass
- Small surface mounting type
- Lead-free parts meet RoHS requirements

Mechanical data

- Case : GLASS MINI-MELF / SOD-80
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Polarity : Indicated by cathode band
- Mounting Position : Any
- Weight : Approximated 0.03 gram

Package outline



Maximum ratings (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Conditions	Symbol	MIN.	TYP.	MAX.	Unit
Repetitive peak reverse voltage		V_{RRM}			100	V
Reverse voltage		V_R			75	V
Peak forward surge current	$t_p = 1\mu\text{s}$	I_{FSM}			2000	mA
Non-repetitive peak forward current		I_{FM}			450	mA
Average forward current		I_{FAV}			150	mA
Power dissipation	$T_A = 25^{\circ}\text{C}$	P_D			500	mW
Operating junction temperature range		T_J	-55		+150	$^{\circ}\text{C}$
Storage temperature range		T_{STG}	-65		+150	$^{\circ}\text{C}$

Electrical characteristics (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Conditions	Symbol	MIN.	TYP.	MAX.	Unit
Forward voltage	$I_F = 10\text{ mA}$	V_F			1.00	V
Reverse current	$V_R = 20\text{ V}$	I_R			25	nA
	$V_R = 20\text{ V}, T_J = 150^{\circ}\text{C}$	I_R			50	μA
	$V_R = 75\text{ V}$	I_R			5.0	μA
Diode capacitance	$V_R = 0\text{ V}, f = 1\text{ MHz}$	C_T			4.0	pF
Reverse recovery time	$I_F = 10\text{ mA}, V_R = 6\text{ V}, I_{RR} = 1\text{ mA}, R_L = 100\Omega$	t_{rr}			4.0	ns

Rating and characteristic curves (LL4148 / MM4148)

Figure 1. Power Dissipation vs Ambient Temperature

Valid provided leads at a distance of 0.8mm from case are kept at ambient temperature

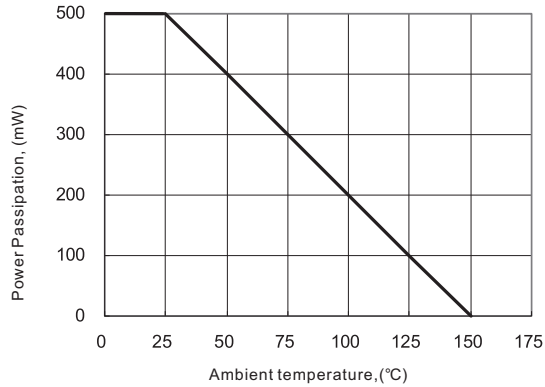


Figure 2. Total Capacitance

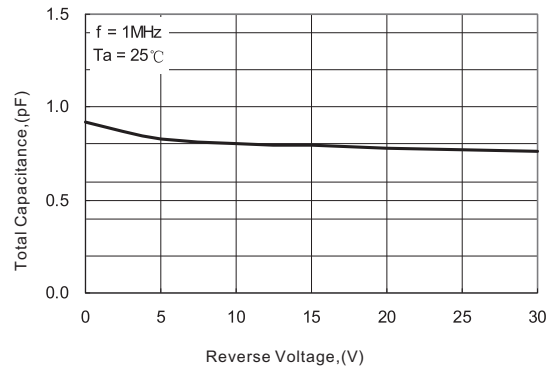


Figure 3. Reverse Voltage vs Reverse Current

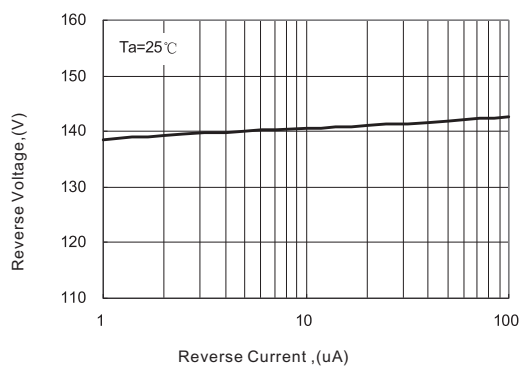


Figure 4. Reverse Current vs Reverse Voltage

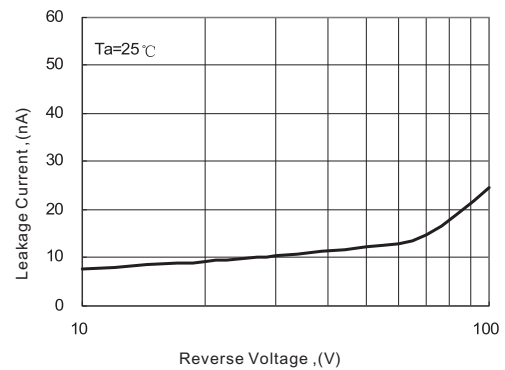


Figure 5. Forward Voltage vs Forward Current

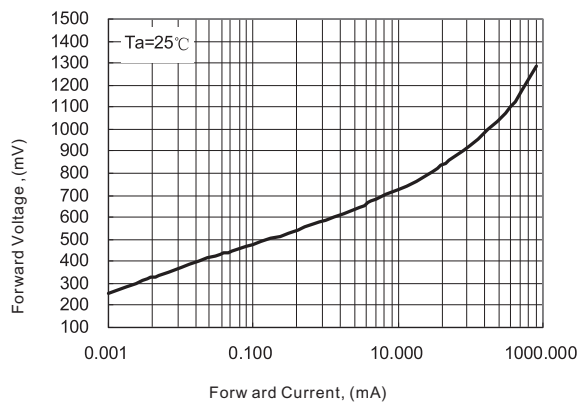
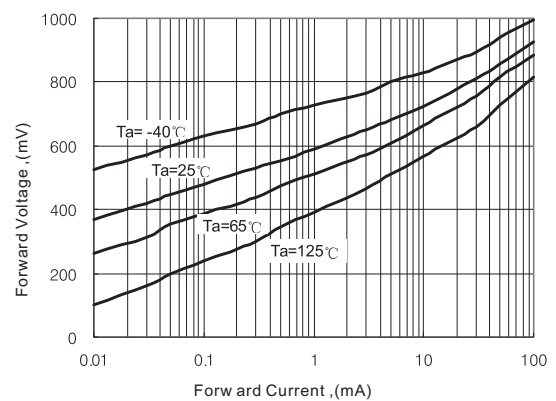


Figure 6. Forward Voltage vs Ambient Temperature



LL4148 (MM4148)

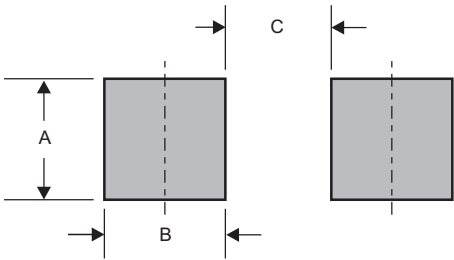
Pinning information

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode		

Marking

Type number	Marking code
LL4148	cathode band only

Suggested solder pad layout

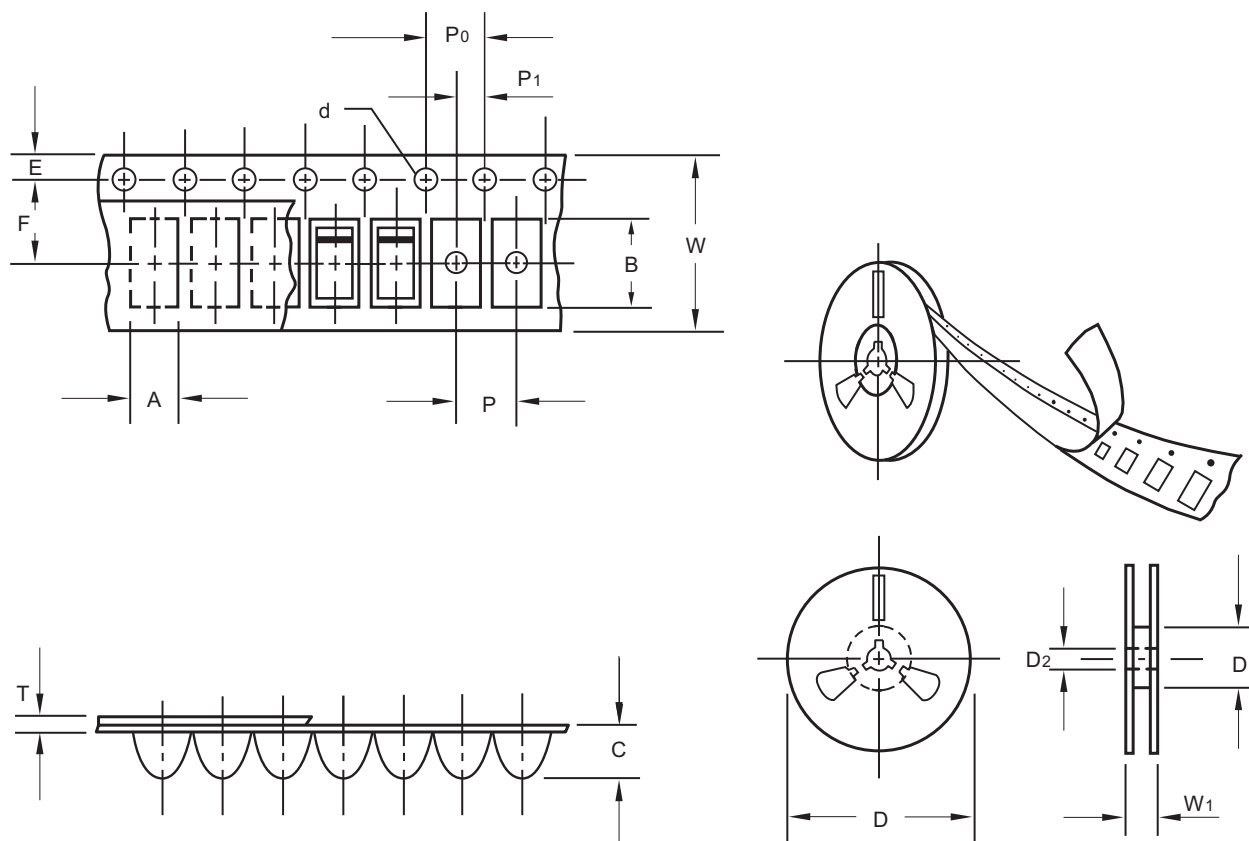


Dimensions in inches and (millimeters)

PACKAGE	A	B	C
SOD-80	0.071(1.80)	0.035(0.90)	0.102(2.60)

LL4148 (MM4148)

Packing information



unit:mm

Item	Symbol	Tolerance	SOD-80
Carrier width	A	0.1	2.00
Carrier length	B	0.1	3.70
Carrier depth	C	0.1	1.80
Sprocket hole	d	0.1	1.50
7" Reel outside diameter	D	2.0	178.00
7" Reel inner diameter	D1	min	50.00
Feed hole diameter	D2	0.5	13.00
Sprocket hole position	E	0.1	1.75
Punch hole position	F	0.1	3.50
Punch hole pitch	P	0.1	4.00
Sprocket hole pitch	P0	0.1	4.00
Embossment center	P1	0.1	2.00
Overall tape thickness	T	0.1	0.23
Tape width	W	0.3	8.00
Reel width	W1	1.0	11.40

Note: Devices are packed in accordance with EIA standard RS-481-A and specifications listed above.

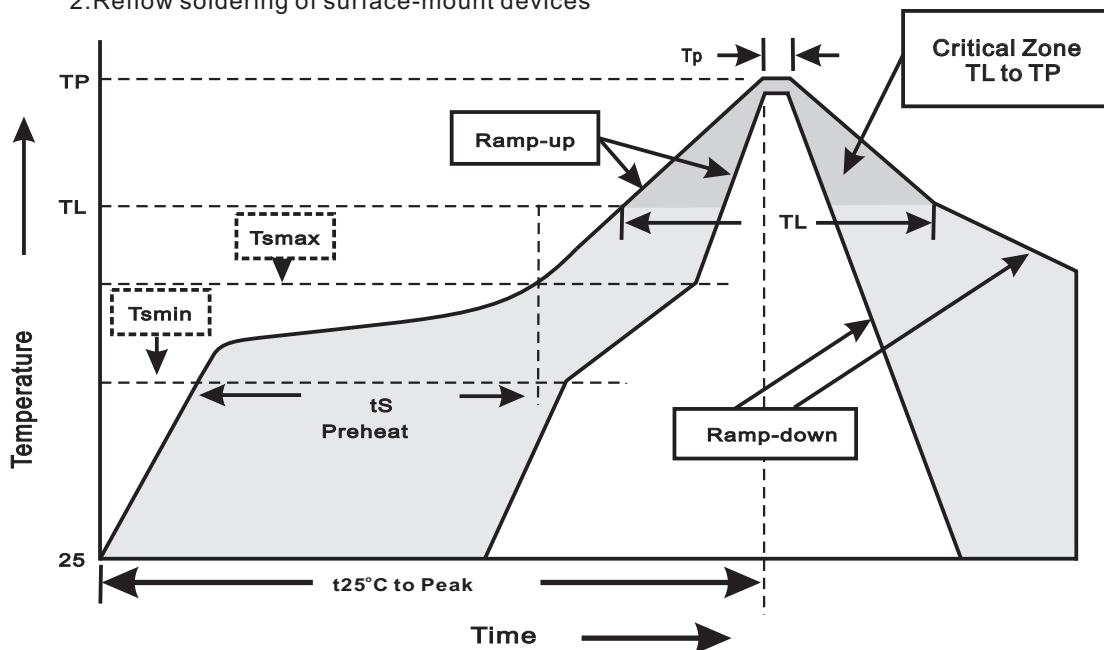
LL4148 (MM4148)

Reel packing

PACKAGE	REEL SIZE	REEL (pcs)	COMPONENT SPACING (m/m)	BOX (pcs)	INNER BOX (m/m)	REEL DIA, (m/m)	CARTON SIZE (m/m)	CARTON (pcs)	APPROX. GROSS WEIGHT (kg)
SOD-80	7"	2,500	4.0	25,000	183*123*183	178	382*257*387	200,000	9.6

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(T _L to T _P)	<3°C/sec
Preheat -Temperature Min(T _{smmin}) -Temperature Max(T _{smmax}) -Time(min to max)(t _s)	150°C 200°C 60~120sec
T _{smmax} to T _L -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(T _L) -Time(t _L)	217°C 60~260sec
Peak Temperature(T _P)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(t _P)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes

LL4148 (MM4148)**High reliability test capabilities**

Item Test	Conditions	Reference
1. Solder Resistance	at $260 \pm 5^\circ\text{C}$ for $10 \pm 2\text{sec.}$	MIL-STD-750D METHOD-2031
2. Solderability	at $245 \pm 5^\circ\text{C}$ for 5 sec.	MIL-STD-202F METHOD-208
3. High Temperature Reverse Bias	$V_R = 80\%$ rate at $T_J = 150^\circ\text{C}$ for 168 hrs.	MIL-STD-750D METHOD-1038
4. Forward Operation Life	Rated average rectifier current at $T_A = 25^\circ\text{C}$ for 500hrs.	MIL-STD-750D METHOD-1027
5. Intermittent Operation Life	$T_A = 25^\circ\text{C}$, $I_F = I_O$ On state: power on for 5 min. off state: power off for 5 min. on and off for 500 cycles.	MIL-STD-750D METHOD-1036
6. Pressure Cooker	$15P_{SIG}$ at $T_A = 121^\circ\text{C}$ for 4 hrs.	JESD22-A102
7. Temperature Cycling	-55°C to $+125^\circ\text{C}$ dwelled for 30 min. and transferred for 5min. total 10 cycles.	MIL-STD-750D METHOD-1051
8. Forward Surge	Peak Forward Current at $t_p = 1 \mu\text{s}$	MIL-STD-750D METHOD-4066-2
9. Humidity	at $T_A = 85^\circ\text{C}$, RH=85% for 1000hrs.	MIL-STD-750D METHOD-1021
10. High Temperature Storage Life	at 175°C for 1000 hrs.	MIL-STD-750D METHOD-1031